

# CURRICULIAM VITAE

## **Santosh Prasad Singh**

Assistant Professor

*Department of Electronics and Instrumentation Engineering*

*Sir Chhotu Ram Institute of Engineering and Technology,*

*ChaudharyCharan Singh University Campus,*

*Meerut (UP)-250005*

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### **Work Experience:-**

#### **1. Teaching Experience :- ( 13+ years)**

**Organization:** Sir Chhotu Ram Institute of Engineering & Technology, ChaudharyCharanSingh  
University Campus, Meerut( UP).

**Duration:**From17 nov 2006 to till date.

#### **Responsibilities:-**

- 1 Assistant Dean Student Welfare**
- 2 Nodal officer of scholarship**
- 3 Assistant Controller of Evaluation**
- 4 Teaching**
- 5 Ex Co-Ordinator of Branch**
- 6 Internal Flying squad**
- 7 Internal Examination co**
- 8 Project Coordinator
- 8 Internal Exam Coordinator
- 9 Organizing Seminars, Industrial-Tours for Students.

### **Area of Specialisation:-**

- Digital Electronics
- Control System

### **Academic Qualification:-**

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- M.Tech degree in VLSI Engineering from Shobhit University, Meerut, in 2012 with CGPA of 7.84(74.48) based on 10 point grade system.
- B.Tech degree in Electronics and Communication Engineering from RadhaGovind Engineering College, Meerut, UP Technical University (UPTU), Lucknow in 2004 with aggregate percentage of 62.76

**Project:-**

- Study And Analysis of FPGA based VLSI design of 8 bit microcontroller processor  
Dissertation of M.Tech
- Digital Controlled variable Power Supply

**Industrial Training:-**

One month Industrial Training in HydroElectric Power Corporation

**Industrial Visit:-**

Visit to Solar Energy Centre, Faridabad

**Technical Skills:-**

- Operating System : Window7/XP/2000/98/Vista/ office XP.
- PCB Design Tool : Eagle-5.4.0, ORCAD designs suite v16.3, PCB Wizard.
- Language : Basic Knowledge of C, VHDL, MATLAB
- VLSI Tool : Cadence Virtuoso 6.1.6, Tanner 12.5,14.1 (Circuit Simulation)
- VHDL : Model SIM v6.5de(HDL Simulation)
- ISE 9.1 : Synthesis tool for generation of Jedec or bit File  
toprogram FPGA or CPLD
- Keil :  $\mu$ Version3 (code Simulation , HEX file generation)

**Award Received:-**

Certificate for appreciation for Balika Suraksha Jagrukata Aviyan KAVACH 2019

**Short-Term Courses/Workshop Attended:-**

- I. Workshop on "MATLAB" held at SCRIET, CCS University, Meerut on 21<sup>st</sup> February, 2015 conducted by CETPA INFOTECH PVT. LTD.
- II. Workshop on "Technology Development and Transfer to Industry-Challenges & Opportunities" held at CCS University, Meerut on 31 March, 2015.

- III. QIP Workshop on “Experiments for Robotics Class” at IIT-Roorkee on 13<sup>th</sup> February, 2016.
- IV. Workshop organized by Russa on NEW POLICY OF EDUCATION on 16 Jan 2016

**Seminar/Conference Attended:-**

- 1 **Certificate of participation on faculty development on Technology in VLSI & Embedded System on dated 25 Jan to 07 Feb 2010**
- 2 National seminar on “Energy And Environment Problems and Solutions In 21<sup>st</sup> Century” Organized by SCRIET, CCS University Campus, Meerut on 30-31 March, 2009.
- 3 National Seminar on Disaster Management In Collaboration with ICEIT, New Delhi held at RadhaGovind Group Of Institutions, Meerut on 10 November, 2012.
- 4 International conference on Recent Technologies in Advance Electronics April 11 and 12 2013.
- 5 National seminar on “Application of Remote Sensing and GIS” Organized by CCS University, Meerut on 30-31 March, 2015.
- 6 Industrial Automation –PLC & Automation workshop of Technex 17 in Deewan Vs Institute of Engineering and Technology on dated 28 & 29 Sep 2016
- 7 Faculty development Program on application of Advance Microprocessor & Microcontroller in Smart Energy Delivery System on 4 to 8 June 2018

**Personal Details:-**

Father's name : Mr. Sita Ram Singh  
Mother's name : Mrs. Rasmuni Devi  
Date of Birth : 25-July-1978  
Sex : Male  
Nationality : Indian  
Marital Status : Married  
Language Known : English & Hindi  
Address of  
correspondance : B-32 flat no 203  
KunjVihar sector 6  
JagritiVihar Meerut U.P

**Declaration:-**

I hereby declare all information furnished above is authentic to best of my knowledge.

Place: Meerut

Santosh Prasad Singh